

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

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1-34. (Cancelled)

35. (Previously Presented) A driver circuit, comprising:

a first storage capacitor;

a second storage capacitor;

an n-channel transistor, of which a gate is connected to the first storage capacitor; and

a p-channel transistor, of which a gate is connected to the second storage capacitor,

a current driven element being disposed between the n-channel transistor and the p-channel transistor,

a data current according to a data signal flowing through the p-channel transistor and the n-channel transistor so that a first operating voltage of the n-channel transistor and a second operating voltage of the p-channel transistor are set by the first storage capacitor and the second storage capacitor, and

the n-channel transistor and the p-channel transistor operatively controlling, in combination, a driving current according to the data signal supplied to a current driven element.

36. (Previously Presented) The driver circuit as claimed in claim 35,

further comprising first switching means,

the first switching means and a source of the data current being connected so as to provide when operative a current source for the current driven element.

37. (Previously Presented) The driver circuit as claimed in claim 35,

further comprising first switching means,  
the first switching means and a source of the data current being connected so  
as to provide when operative a current sink for the current driven element.

38. (Previously Presented) The driver circuit as claimed in claim 35,  
further comprising a second switching means,  
the second switching means being connected to bias the n-channel transistor  
and the p-channel transistor to act as diodes respectively when the data current flows through  
the n-channel transistor and p-channel transistor.

39. (Previously Presented) The driver circuit as claimed in claim 35,  
the n-channel transistor and the p-channel transistor being polysilicon thin film  
transistors.

40. (Previously Presented) The driver circuit as claimed in claim 35,  
the current driven element being an electroluminescent element.

41. (Previously Presented) The driver circuit as claimed in claim 35,  
the n-channel transistor and the p-channel transistor being arranged in close  
proximity to each other.

42. (Previously Presented) A driving method of a driver circuit that is for a  
current driven element and that has an n-channel transistor, a p-channel transistor, the current  
driven element being disposed between the n-channel transistor and the p-channel transistor,  
a first storage capacitor connected to a gate of the n-channel transistor, and a second storage  
capacitor connected to a gate of the p-channel transistor, comprising:

a first step for setting a first operating voltage of the n-channel transistor and a  
second operating voltage of the p-channel transistor by supplying a data current according to  
a data signal to the n-channel transistor and the p-channel transistor; and

a second step for supplying a current that is controlled by the n-channel transistor and the p-channel transistor in combination to the current driven element.

43. (Previously Presented) The driving method as claimed in claim 42, in the first step, the n-channel transistor and the p-channel transistor acting as a diode.

44. (Previously Presented) The driving method as claimed in claim 42, the current driven element being an electroluminescent element.

45. (Previously Presented) An electro-optical device comprising the driver circuit according to claim 35.

46. (Previously Presented) An electronic apparatus incorporating an electro-optical device according to claim 45.

47. (Currently Amended) A driver circuit for a current driven element, comprising:

a storage capacitor;

<sup>T<sub>12</sub> or T<sub>15</sub></sup>  
a driving transistor of which a gate is connected to the storage capacitor;

<sup>T<sub>1</sub></sup>  
an n-channel transistor; and

<sup>T<sub>2</sub></sup>  
a p-channel transistor,

an operating voltage of the driving transistor being set by the storage capacitor by flowing a data current according to a data signal,

a data driving current according to a data signal that flows through the current driven element flowing through the n-channel transistor, the p-channel transistor and the driving transistor so that an operating voltage of the driving transistor is set by the storage capacitor, and

the current driven element being disposed between the n-channel transistor and the p-channel transistor.

48. (Previously Presented) The driver circuit according to claim 47,  
the n-channel transistor and the p-channel transistor being controlled by an  
identical signal.

49. (Previously Presented) A driver circuit, comprising:  
a first storage  <sup>$C_2$</sup>  capacitor;  
a second storage  <sup>$C_1$</sup>  capacitor;  
an n-channel  <sup>$T_{1,n}$</sup>  transistor of which a gate is connected to the first storage  
capacitor;  
a p-channel transistor of which a gate is connected to the second storage  
capacitor;  
a current driven element disposed between the n-channel transistor and the p-  
channel transistor;  
a first switching  <sup>$T_b$</sup>  transistor connected between a drain of the n-channel  
transistor and the first storage capacitor; and  
a second  <sup>$T_1$</sup>  transistor connected between a drain of the p-channel transistor and  
the second storage capacitor.

50. (Previously Presented) A driver circuit, comprising:  
a first storage capacitor;  
a second storage capacitor;  
a first n-channel transistor of which a gate is connected to the first storage  
capacitor;  
a first p-channel transistor of which a gate is connected to the second storage  
capacitor;  
a second n-channel transistor;  
a second p-channel transistor;

a current driven element disposed between the second n-channel transistor and the second p-channel transistor;

a first switching transistor connected between a drain of the first n-channel transistor and the first storage capacitor; and

a second switching transistor connected between a drain of the first p-channel transistor and the second storage capacitor.

51. (Previously Presented) The driver circuit according to claim 50, the second n-channel transistor and the second p-channel transistor being controlled by an identical signal.

52. (Previously Presented) The driver circuit according to claim 50, the first n-channel transistor being connected to the first p-channel transistor.

53. (Previously Presented) The driver circuit as claimed in claim 50, the current driven element being an organic electroluminescent element.

54. (Previously Presented) An electro-optical device comprising the driver circuit according to claim 50.

55. (Previously Presented) An electronic apparatus incorporating an electro-optical device according to claim 54.

56. (Previously Presented) The driver circuit as claimed in claim 49, the current driven element being an organic electroluminescent element.

57. (Previously Presented) An electro-optical device comprising the driver circuit according to claim 49.

58. (Previously Presented) An electronic apparatus incorporating an electro-optical device according to claim 57.

59. (New) A driver circuit for driving a current driven element, the driver circuit comprising:

fig 5

a first <sup>n</sup>transistor;

a second <sup>p</sup>transistor; and

a data current according to a data signal determining a first operating voltage of the first transistor and a second operating voltage of the second transistor,

the first transistor being an n-channel transistor,

the second transistor being a p-channel transistor, and

a driving current that is supplied to the current driven element flowing through the first transistor and the second transistor.

DI  
cmt

60. (New) The driver circuit according to claim 59, further comprising:

a first storage capacitor connected to a first gate of the first transistor; and

a second storage capacitor connected to a second gate of the second transistor,

the first storage capacitor setting the first operating voltage, and

the second storage capacitor setting the second operating voltage.

61. (New) The driver circuit according to claim 60,

the first storage capacitor being disposed between a first source and the first gate of the first transistor, and

the second storage capacitor being disposed between a second source and the second gate of the second transistor.

62. (New) The driver circuit according to claim 59,

the current driven element being disposed between the first transistor and the second transistor.

63. (New) The driver circuit according to claim 59, further comprising:

a switching device controlling electrical connection between the current source of the data current and one of the first transistor and the second transistor.

64. (New) The driver circuit according to claim 59, further comprising:

a switching device controlling electrical connection between the current sink of the data current and one of the first transistor and the second transistor.

65. (New) The driver circuit according to claim 61, further comprising:

a switching device controlling electrical connection between the first source and the first gate and controlling electrical connection between the second source and the second gate.

66. (New) The driver circuit according to claim 59,

the first transistor and the second transistor being polysilicon thin film transistors.

67. (New) The driver circuit according to claim 59,

the current driven element being an electroluminescent element.

68. (New) The driver circuit according to claim 59,

the first transistor and the second transistor being disposed in close proximity to each other.

69. (New) A driving method to drive a driving circuit for a current driven

element, the driving method comprising:

a first step for setting a first operating voltage of a first transistor and a second operating voltage of a second transistor by flowing a data current to a data signal; and

a second step for supplying a driving current to the current driven element through the first transistor and the second transistor.

70. (New) The driver circuit according to claim 69,

in the first step, the first transistor and the second transistor act as diodes.

71. (New) An electro-optical device comprising the driver circuit according to

claim 59,

72. (New) The driver circuit according to claim 59, further comprising:

a first switching transistor; and  
a second switching transistor,  
the first switching transistor being disposed between a first drain of the first transistor and a first gate of the first transistor, and  
the second switching transistor being disposed between a second drain of the second transistor and a second gate of the second transistor.

73. (New) The driver circuit according to claim 59, further comprising:

*DI added*  
a third switching transistor being an n-channel transistor; and  
a fourth switching transistor being a p-channel transistor,  
the current driven element being disposed between the third transistor and the fourth transistor.

the second switching transistor being disposed between a second drain of the second transistor and a second gate of the second transistor.

74. (New) The driver circuit according to claim 73,

the third and fourth transistors being controlled by an identical signal.

75. (New) The driver circuit according to claim 74,

the first and second transistors being controlled in series.

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